

Product Specification

SPECIFICATION FOR APPROVAL

(●) Preliminary Specification

() Final Specification

Title	32.0" QWUXGA TFT LCD
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BUYER	Common
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	RS320EUY
SUFFIX	GD25 (RoHS Verified)

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY	SIGNATURE	DATE
/		
/		
/		

Please return 1 copy for your confirmation with
your signature and comments.

APPROVED BY	SIGNATURE	DATE
Ki Yol Chong / Team Leader		
REVIEWED BY Hyuck Jin Kim / Project Leader		
PREPARED BY Sung Hoon Choi / Engineer		

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RECORD OF REVISIONS

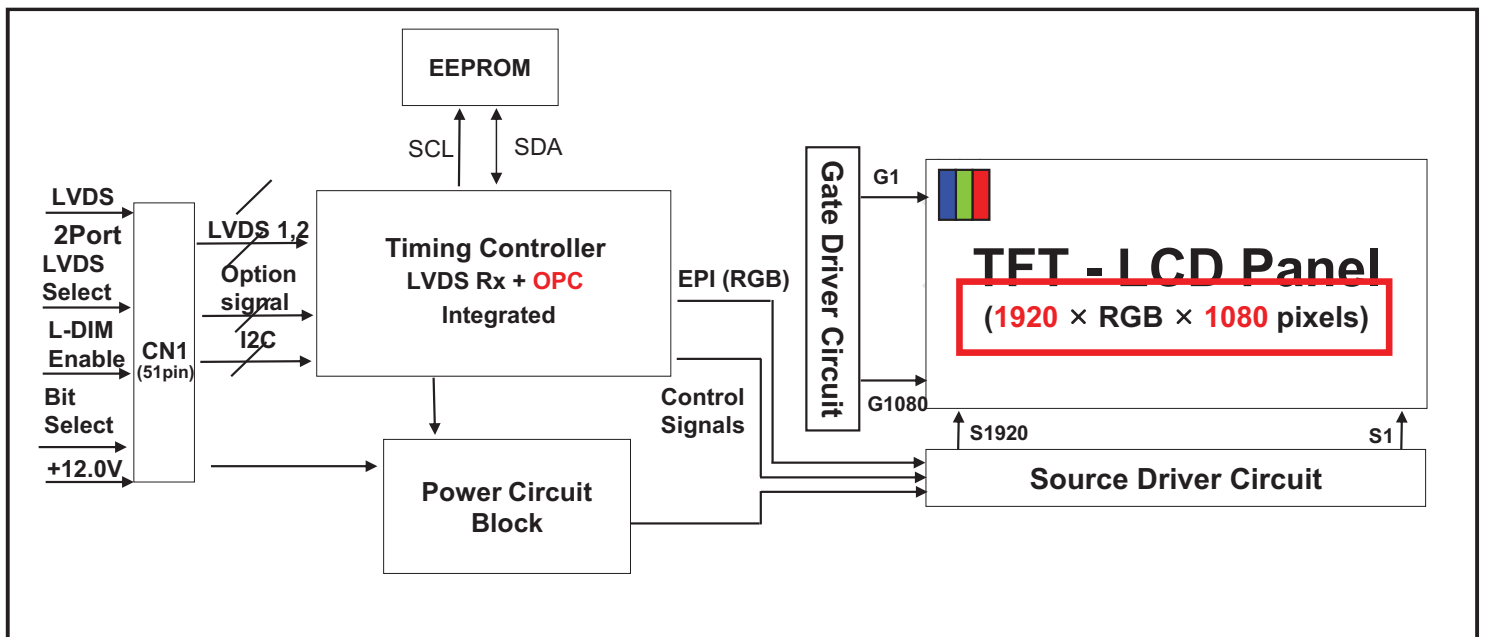
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1. General Description

The RS320EUY is a Color Active Matrix Liquid Crystal Display with an integral the Source PCB and Gate implanted on Panel (GIP). The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 31.55 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green, Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit(D) gray scale signal for each dot. Therefore, it can present a palette of more than 1.07Bilion colors.

It has been designed to apply the 8-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	31.55 inches diagonal
Outline Dimension	708.4(H) x 405.4 (V) x 1.2 mm(D) (Typ.)
Pixel Pitch	0.36375 mm x 0.36375 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels
Display Mode	IPS
Color Depth	10-bit(D), 1.07 Billion colors
Interface	LVDS 2 port
Display brightness	2500 (Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption (oc)	Logic= 4.26W (Typ.), 6.87W (Max.) (TBD)
Weight	0.78Kg (Typ.) , 0.80Kg (Max.) (TBD)
Display Mode	Transmissive mode, Normally black
Surface Treatment (Top)	Hard coating(2H), Anti-glare low reflection treatment of the front polarizer (Haze 3%(Typ.))

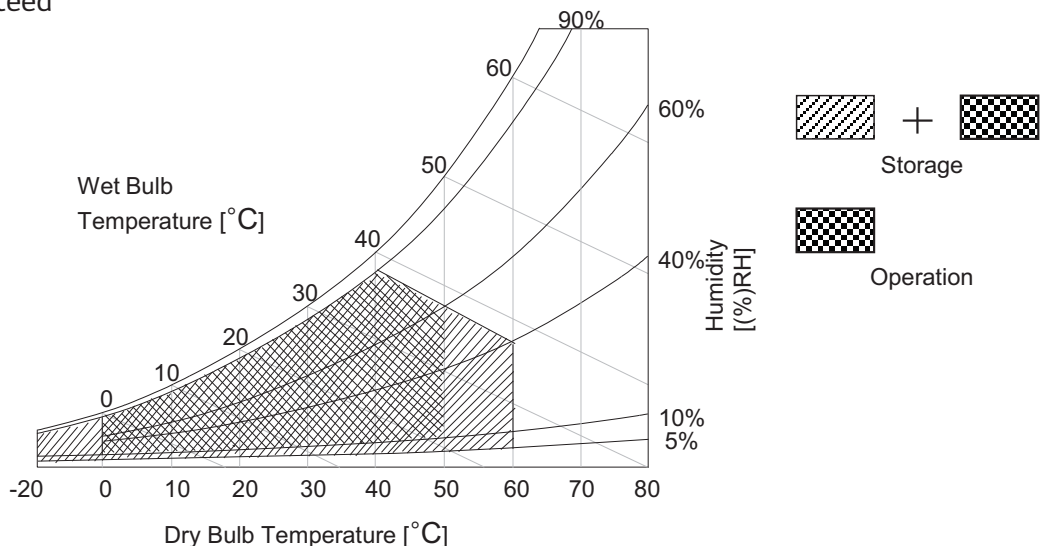
2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Note
			Min	Max		
Power Input Voltage	LCD Circuit	V _{LCD}	-0.3	+14.0	V _{DC}	1
T-Con Option Selection Voltage		V _{LOGIC}	-0.3	+4.0	V _{DC}	
Operating Temperature		T _{OP}	-20	+70	°C	2,3
Storage Temperature(without packing)		T _{ST}	-25	+75	°C	
Panel Front Temperature (Considering L/C Phase Transition Temperature)		T _{PT}	-	+68	°C	4
Operating Ambient Humidity		H _{OP}	10	90	%RH	2,3
Storage Humidity		H _{ST}	5	90	%RH	

- Note 1. Ambient temperature condition ($T_a = 25 \pm 2^\circ\text{C}$)
2. Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be Max 39°C, and no condensation of water.
3. Gravity mura can be guaranteed below 40°C and under backlight luminance 350nit condition.
4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design.
5. Prevent products from being exposed to the direct sunlight. Otherwise, its reliability and function may not be guaranteed



3. Electrical Specifications

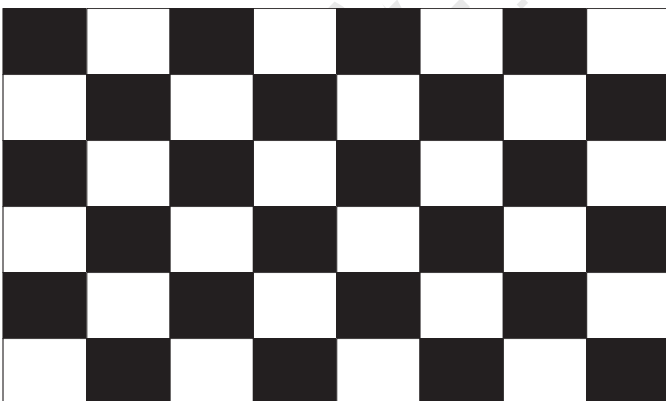
3-1. Electrical Characteristics

Table 2. ELECTRICAL CHARACTERISTICS

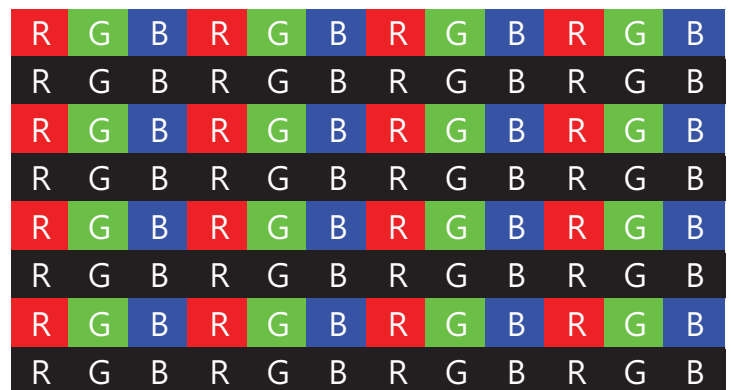
Parameter		Symbol	Value			Unit	Note
			Min	Typ	Max		
Circuit :							
Power Input Voltage		V _{LCD}	10.8	12.0	13.2	V _{DC}	4
Power Input Current		I _{LCD}	-	355(TBD)	465(TBD)	mA	1
			-	440(TBD)	575(TBD)	mA	2
T-CON Option Selection Voltage	Input High Voltage	V _{IH}	2.7	-	3.6	V _{DC}	
	Input Low Voltage	V _{IL}	0	-	0.7	V _{DC}	
Power Consumption		P _{LCD}	-	4.26(TBD)	5.54(TBD)	Watt	1
			-	5.28(TBD)	6.87(TBD)	Watt	2
Rush current		I _{RUSH}	-	-	5.0	A	3

- Note
1. The specified current and power consumption are under the V_{LCD}=12.0V, Ta=25 ± 2°C, f_V=60Hz condition, and mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
 2. The current and power consumption are specified at the maximum current patter.
 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).
 4. Ripple voltage level is recommended under ±5% of typical voltage

White : 255 Gray
Black : 0 Gray



Mosaic Pattern(8 x 6)



Max Current Pattern

3-2. ELECTRICAL CHARACTERISTICS (Continue)

Parameter			Symbol	Values			Unit	Notes
				Min	Typ	Max		
Power Supply Input Voltage			VBL	22.5	24	25.6	Vdc	
Power Supply Input Current			IBL		3.92		A	
Power Consumption (Total)			PBL		94		W	
Input Voltage for Control System Signals	On/Off	On	Von	2		5	Vdc	
		Off	Voff	0		0.5	Vdc	
	External PWM Duty ratio		D_PWM	0		100	%	1
	External PWM Frequency		F_PWM	1k		10k	Hz	

3-2-1. Backlight Module

CN1 PH2.0-14PIN (2.0mm*14)

Pin No. /引脚	Symbol/符号	Description/ 描述	Parameter /参数
1	VCC	Power Supply Voltage	
2	VCC	Power Supply Voltage	
3	VCC	Power Supply Voltage	
4	VCC	Power Supply Voltage	
5	VCC	Power Supply Voltage	
6	GND	Power ground	
7	GND	Power ground	
8	GND	Power ground	
9	GND	Power ground	
10	GND	Power ground	
11	NC	Not connect	
12	N/F	Output enable signal	
13	NC	Not connect	
14	ADJ	Dimming control	0V=Brightness Max 5V=Brightness Min

CN2 PH2.0-14PIN (2.0mm*12)

1	G-	Feedback	Connect to light sense -
2	G+	Power	Connect to light sense +

3-2. Interface Connections

This LCD module employs two kinds of interface connection, 51-pin connector is used for the module electronics.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RE51S-HF(manufactured by JAE) or GT05S-51S-H38(manufactured by LSM) or IS050-C51B-C39(manufactured by UJU) or 05030WR-H51B(manufactured by YEONHO)
- Mating Connector : FI-RE51HL(JAE) or compatible

Table 3. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	NC	No Connection (Note 4)	27	NC	No connection
2	NC	No Connection (Note 4)	28	R2AN	SECOND LVDS Receiver Signal (A-)
3	NC	No Connection (Note 4)	29	R2AP	SECOND LVDS Receiver Signal (A+)
4	NC	No Connection (Note 4)	30	R2BN	SECOND LVDS Receiver Signal (B-)
5	NC	No Connection (Note 4)	31	R2BP	SECOND LVDS Receiver Signal (B+)
6	NC	No Connection (Note 4)	32	R2CN	SECOND LVDS Receiver Signal (C-)
7	LVDS Select	'H' = JEIDA , 'L' or NC = VESA	33	R2CP	SECOND LVDS Receiver Signal (C+)
8	NC	No Connection (Note 4)	34	GND	Ground
9	NC	No Connection (Note 4)	35	R2CLKN	SECOND LVDS Receiver Clock Signal(-)
10	NC	No Connection (Note 4)	36	R2CLKP	SECOND LVDS Receiver Clock Signal(+)
11	GND	Ground	37	GND	Ground
12	R1AN	FIRST LVDS Receiver Signal (A-)	38	R2DN	SECOND LVDS Receiver Signal (D-)
13	R1AP	FIRST LVDS Receiver Signal (A+)	39	R2DP	SECOND LVDS Receiver Signal (D+)
14	R1BN	FIRST LVDS Receiver Signal (B-)	40	NC	No connection
15	R1BP	FIRST LVDS Receiver Signal (B+)	41	NC	No connection
16	R1CN	FIRST LVDS Receiver Signal (C-)	42	NC or GND	No Connection or Ground
17	R1CP	FIRST LVDS Receiver Signal (C+)	43	NC or GND	No Connection or Ground
18	GND	Ground	44	GND	Ground (Note 5)
19	R1CLKN	FIRST LVDS Receiver Clock Signal(-)	45	GND	Ground
20	R1CLKP	FIRST LVDS Receiver Clock Signal(+)	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	R1DN	FIRST LVDS Receiver Signal (D-)	48	VLCD	Power Supply +12.0V
23	R1DP	FIRST LVDS Receiver Signal (D+)	49	VLCD	Power Supply +12.0V
24	NC	No connection	50	VLCD	Power Supply +12.0V
25	NC	No connection	51	VLCD	Power Supply +12.0V
26	NC or GND	No Connection or Ground	-	-	-

- notes
1. All GND(ground) pins should be connected together to the LCD module's metal frame.
 2. All VLCD (power input) pins should be connected together.
 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
 4. #1~#6 & #8~#10 NC (No Connection): These pins are used only for LGD (Do not connect)
 5. Specific pin No. #44 is used for "No signal detection" of system signal interface.
It should be GND for NSB(No Signal Black) during the system interface signal is not.
If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

3-3. Signal Timing Specifications

Table 4 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 4. TIMING TABLE for NTSC & PAL (DE Only Mode)

ITEM		Symbol	Min	Typ	Max	Unit	notes
Horizontal	Display Period	t _{HV}	960	960	960	tCLK	1920 / 2
	Blank	t _{HB}	100	140	240	tCLK	1
	Total	t _{HP}	1060	1100	1200	tCLK	
Vertical	Display Period	t _{VV}	1080	1080	1080	Lines	
	Blank	t _{VB}	20	45	300	Lines	1
	Total	t _{VP}	1100	1125	1380	Lines	

ITEM		Symbol	Min	Typ	Max	Unit	notes
Frequency	DCLK	f _{CLK}	60.00	74.25	78.00	MHz	
	Horizontal	f _H	57.3	67.5	70	KHz	2
	Vertical	f _V	47	60	63	Hz	2

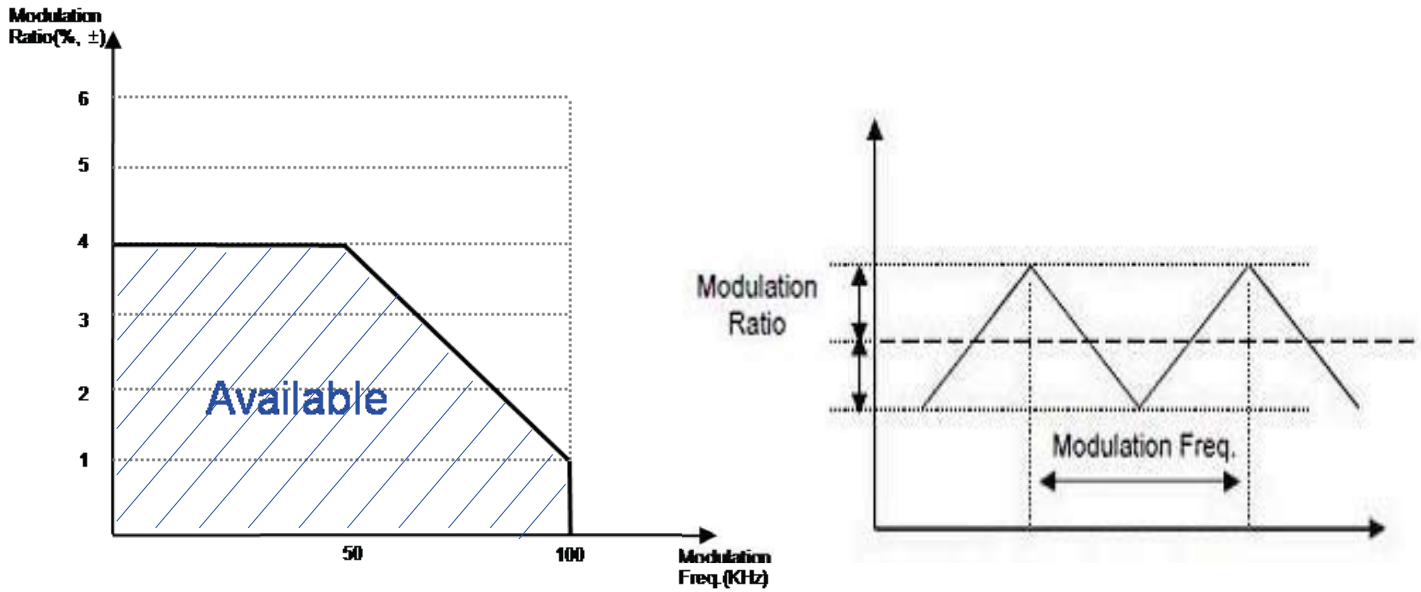
notes: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode).
If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.

2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency

3. Spread Spectrum Rate (SSR) for 50KHz ~ 100kHz Modulation Frequency(FMOD) is calculated by $(7 - 0.06 \cdot F_{mod})$, where Modulation Frequency (FMOD) unit is KHz.
LVDS Receiver Spread spectrum Clock is defined as below figure

※ Timing should be set based on clock frequency.

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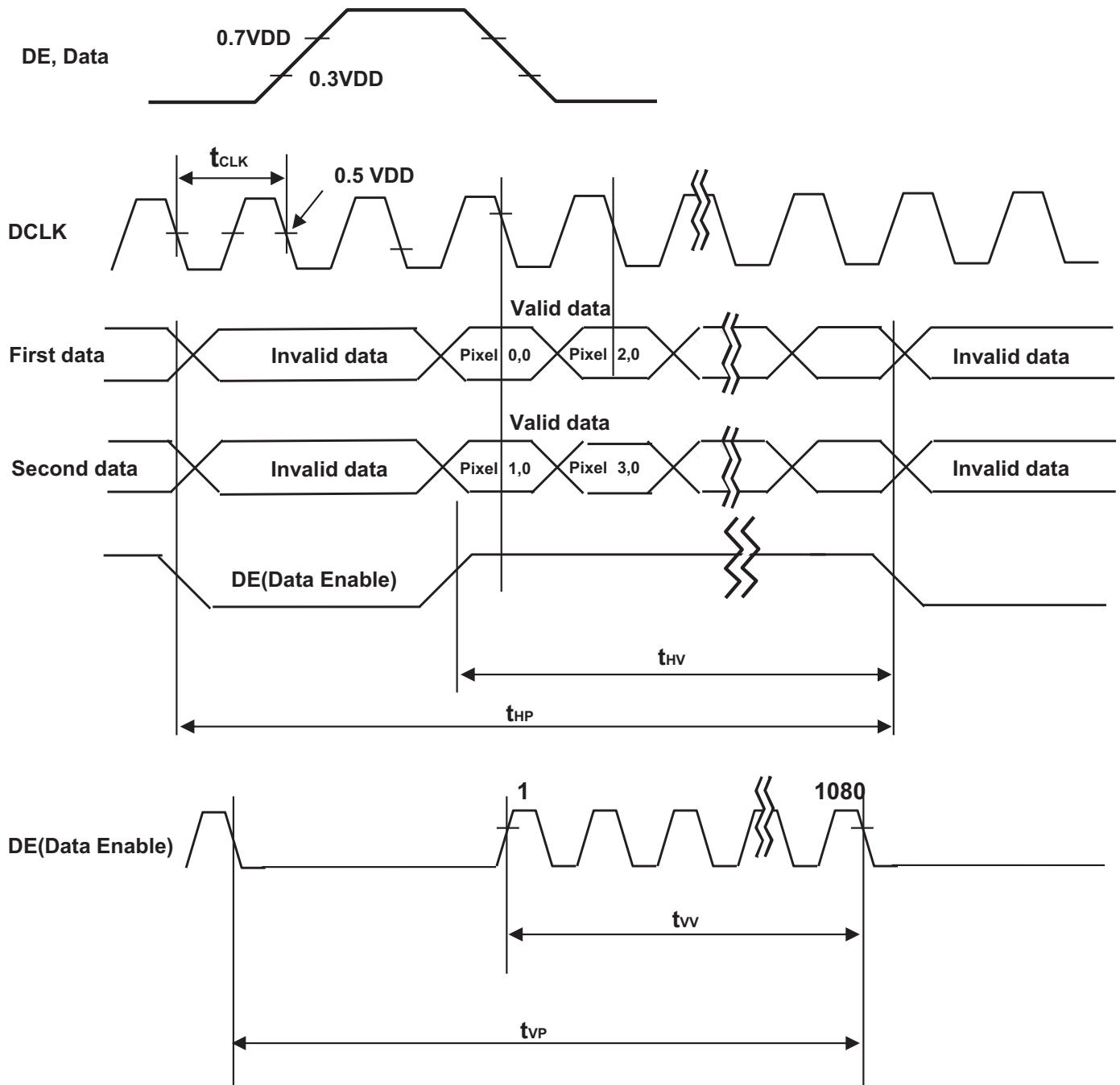


※ Please pay attention to the followings when you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD)

1. Please set proper Spread Spectrum Rate(SSR) and Modulation Frequency (FMOD) of TV system LVDS output.
2. Please check FOS after you set Spread Spectrum Rate(SSR) and Modulation Frequency(FMOD) to avoid abnormal display. Especially, harmonic noise can appear when you use Spread Spectrum under FMOD 30 KHz.

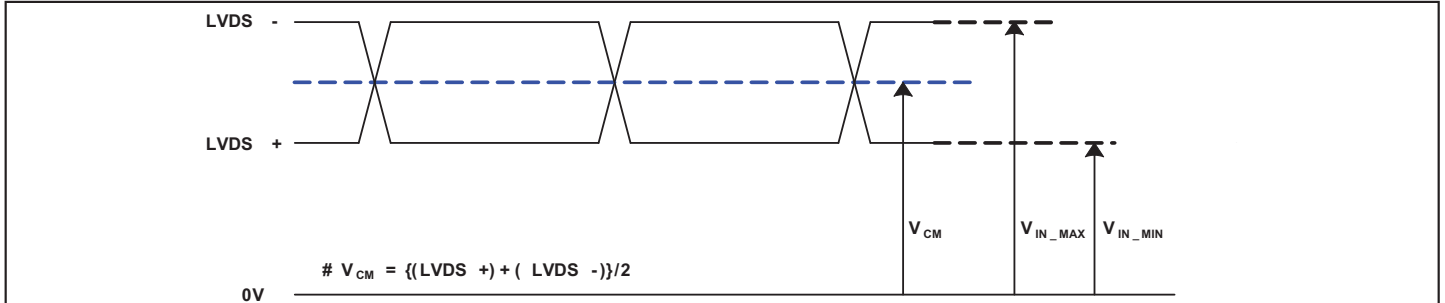
3-4. LVDS Signal Specification

3-4-1. LVDS Input Signal Timing Diagram



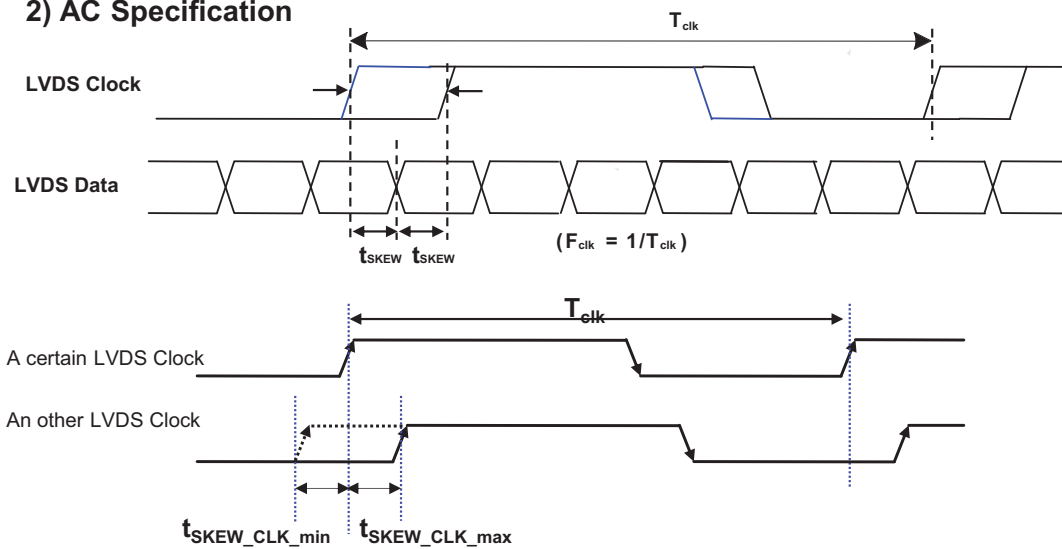
3-4-2. LVDS Input Signal Characteristics

1) DC Specification



Description	Symbol	Min	Max	Unit	notes
LVDS Common mode Voltage	V_{CM}	1.0	1.5	V	-
LVDS Input Voltage Range	V_{IN}	0.7	1.8	V	-
Change in common mode Voltage	ΔV_{CM}	-	250	mV	-

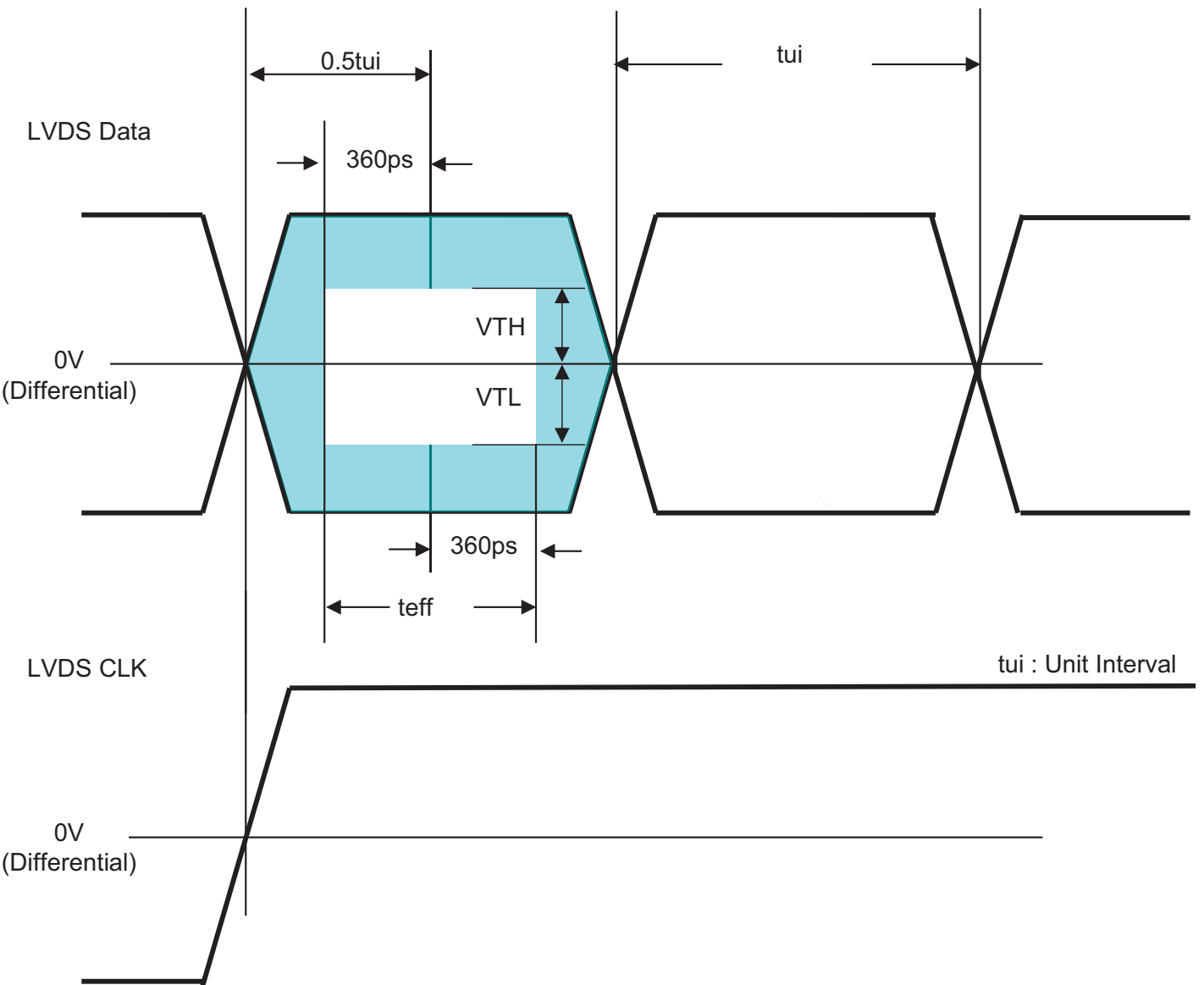
2) AC Specification



Description	Symbol	Min	Max	Unit	notes
LVDS Differential Voltage	V_{TH}	100	600	mV	Tested with Differential Probe 2
	V_{TL}	-600	-100	mV	
LVDS Clock to Data Skew	t_{SKEW}	-	$ 0.20 \cdot T_{clk} / 7 $	ps	-
Effective time of LVDS	t_{eff}	$ \pm 360 $	-	ps	-
LVDS Clock to Clock Skew (Each other)	t_{SKEW_CLK}	-	$ 1/7 \cdot T_{clk} $	ps	-

notes 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

2. LVDS Differential Voltage is defined within t_{eff}



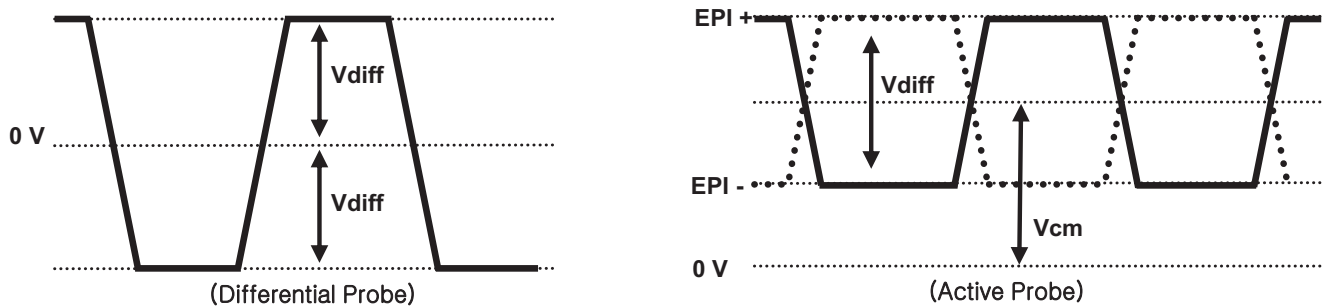
* This accumulated waveform is tested with differential probe

3-5. Intra interface Signal Specification

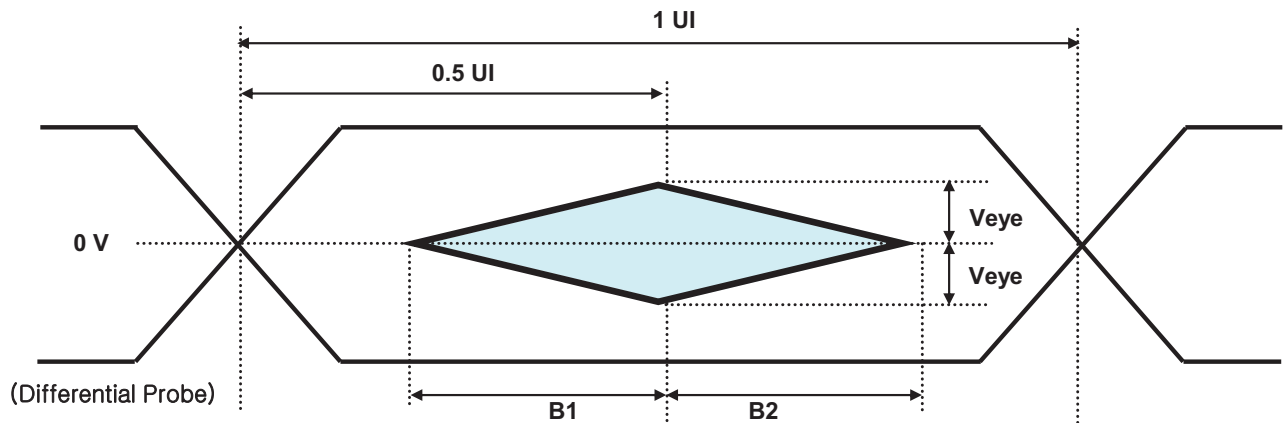
3-5-1. EPI Signal Specification

Table 5. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Condition	MIN	TYP	MAX	Unit	notes
Logic & EPI Power Voltage	VCC	-	1.62	1.8	1.98	V _{DC}	
EPI input common voltage	VCM	LVDS Type	0.8	-	1.3	V	
EPI input differential voltage	V _{diff}	-	150	-	500	mV	
EPI Input eye diagram	V _{eye}	-	90	-	-	mV	
Effective Veye width time	B1&B2		0.25	-	-	UI	



EPI Differential signal characteristics



Eye Pattern of EPI Input

*Source PCB

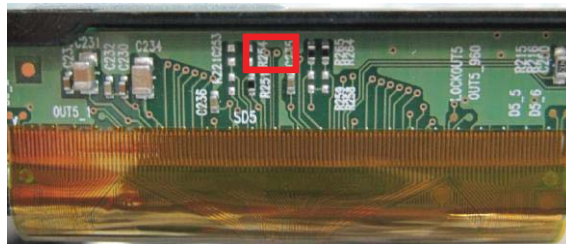


FIG. 1 Measure point

3-6. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8bit gray scale data input for the color. The higher binary input, the brighter the color. Table 6 provides a reference for color versus data input.

Color		Input Color Data																							
		RED								GREEN								BLUE							
		MSB				LSB				MSB				LSB				MSB				LSB			
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...	...								...								...							
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	...	...								...								...							
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	...	...								...								...							
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-7. Power Sequence

3-7-1. LCD Driving circuit

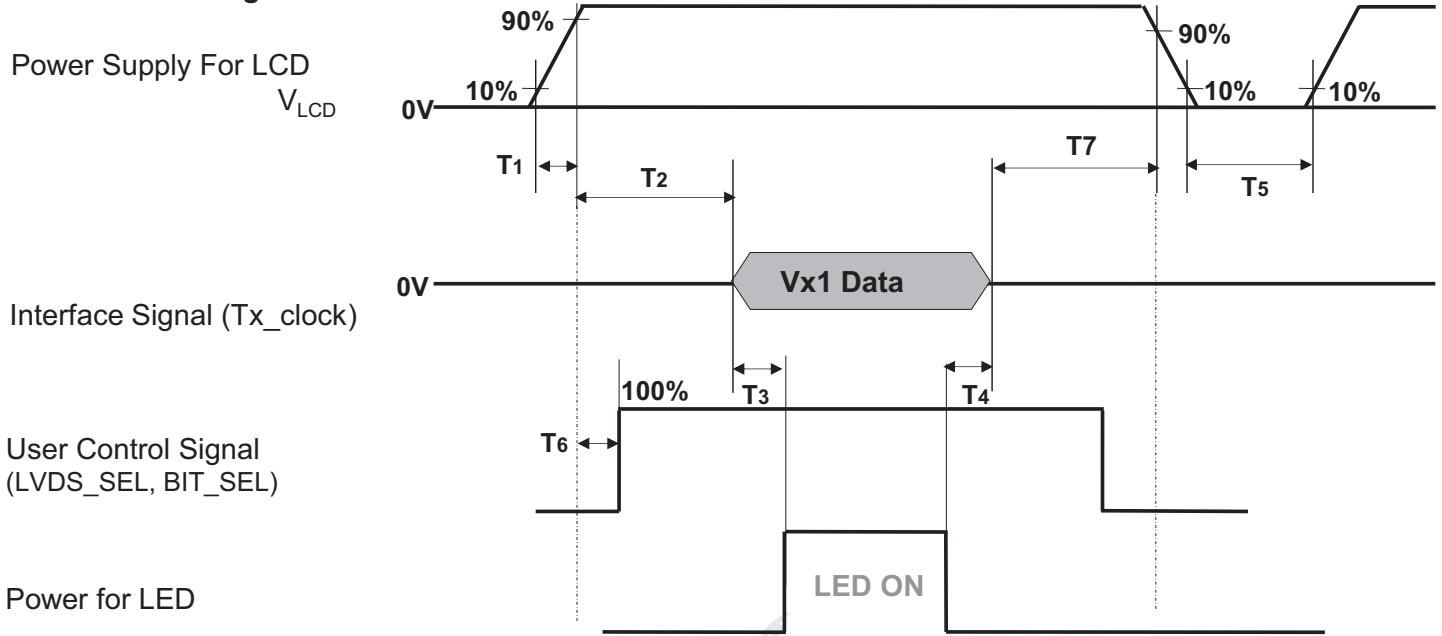


Table 6. POWER SEQUENCE

Parameter	Value			Unit	Note
	Min	Typ	Max		
T1	0.5	-	20	ms	1
T2	0	-	-	ms	2
T3	400	-	-	ms	3
T4	100	-	-	ms	3
T5	1.0	-	-	s	4
T6	0	-	T2	ms	5
T7	0	-	-	ms	6

Note :

1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.
 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
 4. T5 should be measured after the Module has been fully discharged between power off and on period.
 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power (V_{LCD}), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
 6. It is recommendation specification that T7 has to be 0ms as a minimum value.
- ※ Please avoid floating state of interface signal at invalid period.
- ※ When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25\pm 2^{\circ}\text{C}$. The values are specified at 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° . FIG. 1 shows additional information concerning the measurement equipment and method.

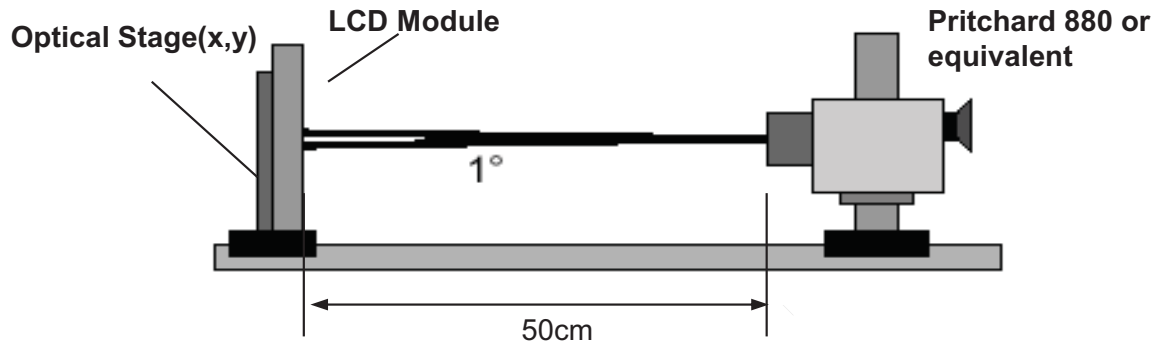


FIG.2 1 Optical Characteristic Measurement Equipment and Method

$T_a = 25\pm 2^{\circ}\text{C}$, $V_{\text{LCD}} = 12.0\text{V}$, $f_v = 60\text{Hz}$, $D_{\text{clk}} = 74.25\text{MHz}$,
Light Source : D65 Standard

Table 7. OPTICAL CHARACTERISTICS

Parameter		Symbol	Value			Unit	Note
			Min	Typ	Max		
Contrast Ratio		CR	900	1300	-		1
Response Time	Variation	G to G _σ		6	9		3
	Gray to Gray (BW)	G to G _{BW}		9	13	ms	2
Transmittance		T	5.175	5.75		%	4
Color Coordinates [CIE1931]	RED	Rx	Typ -0.03	0.661 (TBD)	Typ +0.03		5
		Ry		0.326 (TBD)			
	GREEN	Gx		0.281 (TBD)			
		Gy		0.586 (TBD)			
	BLUE	Bx		0.135 (TBD)			
		By		0.125 (TBD)			
Viewing Angle (CR>10)							
	x axis, right(φ=0°)	θr	89	-	-	degree	6
	x axis, left (φ=180°)	θl	89	-	-		
	y axis, up (φ=90°)	θu	89	-	-		
	y axis, down (φ=270°)	θd	89	-	-		
Gray Scale			-	-	-		7

Product Specification

Note :

1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

It is measured at center 1-point. For more information, see the FIG. 2.

The value of CR should be extracted using the LGD sheet structure (Diffuser/Prism/Prism)

2. Response time is the time required for the display to transit from any gray to white (Rise Time, Tr_R) and from any gray to black (Decay time, Tr_D). For additional information see the FIG. 3.

※ G to G_{BW} Spec stands for average value of all measured points.

Photo Detector : RD-80S / Field : 2 °

The response time is valued with operating condition of LGD's standard BLU

3. G to G_σ is Variation of Gray to Gray response time composing a picture

$$G \text{ to } G(\sigma) = \sqrt{\frac{\sum(X_i - u)^2}{N}}$$

X_i = Individual Data
 u = Data average
 N : The number of Data

4. The value of Transmittance should be extracted using the LGD sheet structure (Diffuser/ Diffuser) and standard light source of D65

5. The value of color coordinates should be extracted using the standard light source of D65

6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.

7. Gray scale specification

Gamma Value is approximately 2.2. For more information, see the Table 10.

Table 8. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ)
L0	TBD
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100

Response time is defined as the following figure and shall be measured by switching the input signal for “Gray(N)” and “Black or White”.

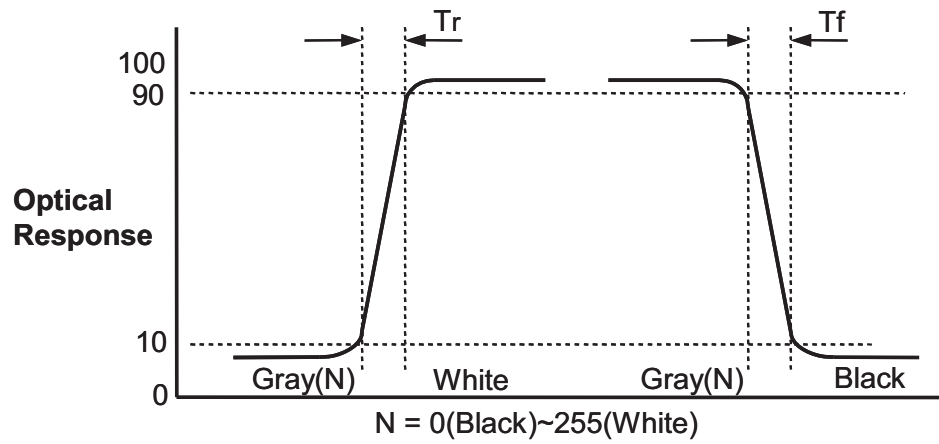


FIG. 3 Response Time

Product Specification

Dimension of viewing angle range

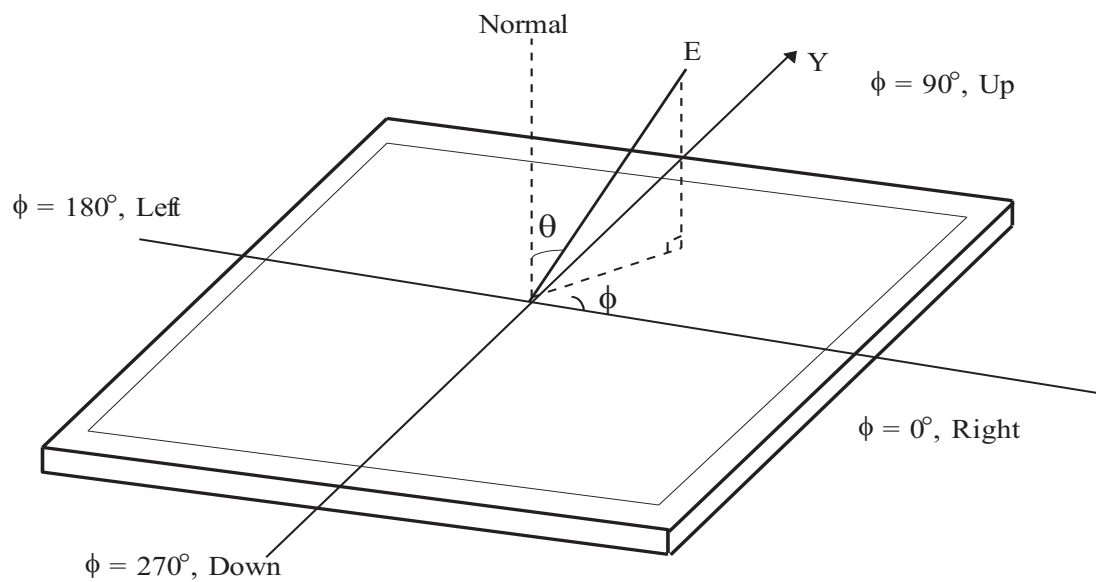


FIG. 4 Viewing Angle

5. Mechanical Characteristics

Table 11 provides general mechanical characteristics.

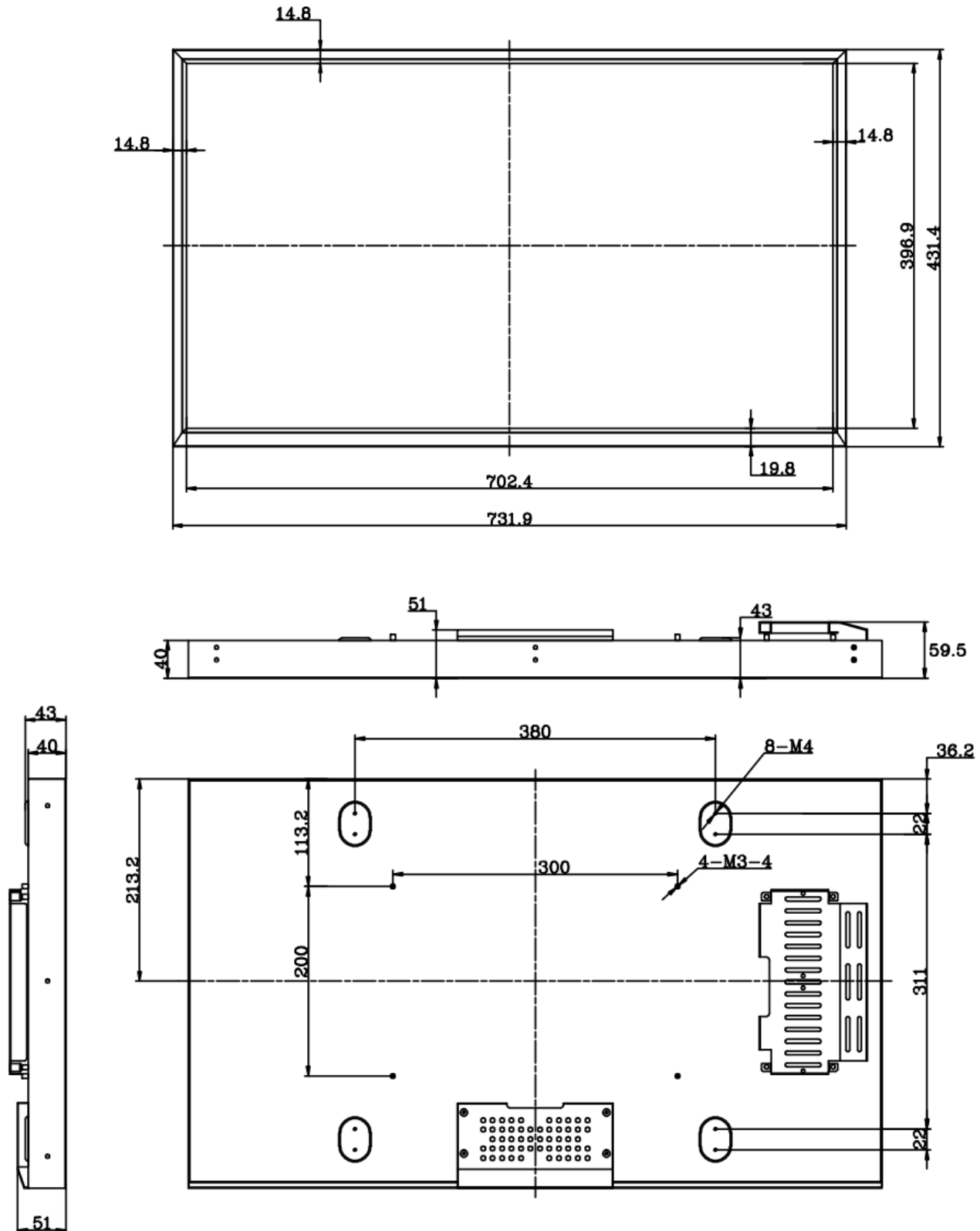
Table 9. MECHANICAL CHARACTERISTICS

Item	Value	
Outline Dimension	Horizontal	731.9mm
	Vertical	431.4mm
	Thickness	40 mm
Active Display Area	Horizontal	698.4 mm
	Vertical	392.85 mm
Weight	8kg(Typ), 8.4kg(Max) (TBD)	
Surface Treatment	Hard coating(2H), Anti-glare low reflection treatment of the front polarizer (Haze 3%(Typ.))	

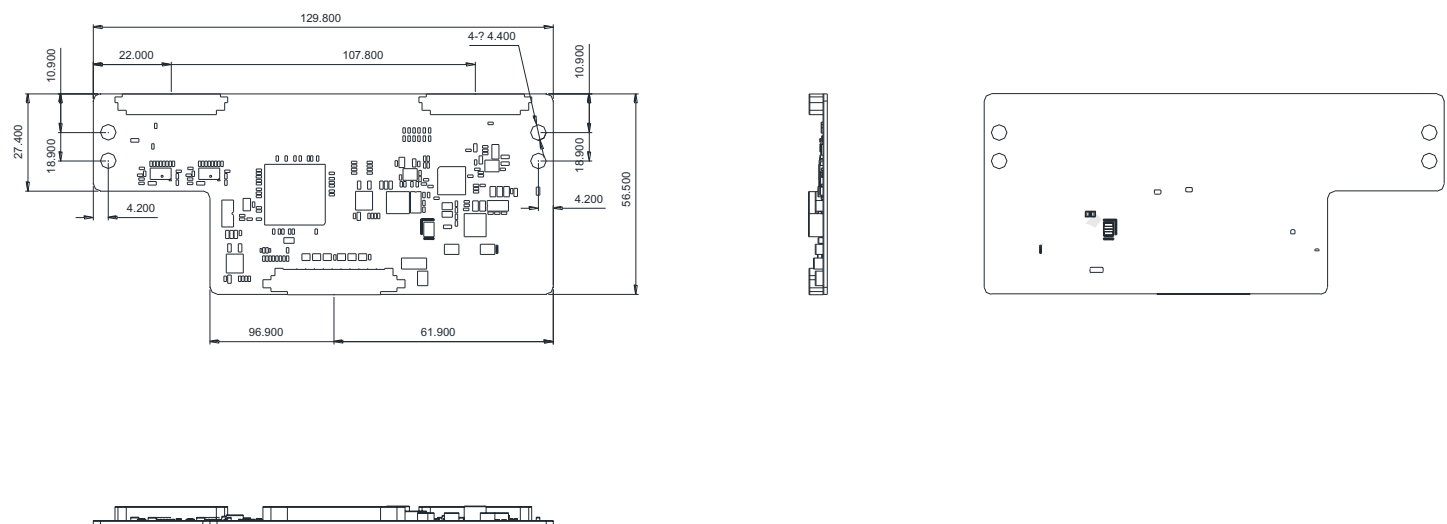
Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

6. Mechanical Dimension

6-1. Board Assembly Dimension



6-2. Control Board Assembly Dimension



Date	Version
Nov. 19, '20	0.1

7. Reliability

Table 10. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 75°C 90% 240h
2	Low temperature storage test	Ta= -25°C 240h
3	High temperature operation test	Ta= 70°C 50%RH 500h
4	Low temperature operation test	Ta= -20°C 500h
5	Humidity condition Operation	Ta= 40 °C ,90%RH
6	Altitude operating storage / shipment	0 - 16,400 ft 0 - 40,000 ft

Note1 : Before and after Reliability test, LCM should be operated with normal function.

8. International Standards

8-1. Safety

- a) IEC 62368-1, The International Electro-technical Commission(IEC).
Audio/video, Information and Communication Technology Equipment - Safety - Safety Requirements.
- b) EN 62368-1, European Committee for Electro-technical Standardization (CENELEC)
Audio/video, Information and Communication Technology Equipment - Safety Requirements
- c) UL 62368-1, UL LLC.
Audio/video, Information and Communication Technology Equipment - Safety Requirements
- d) CAN/CSA C22.2 No.62368-1, Canadian Standards Association (CSA).
Audio/video, Information and Communication Technology Equipment - Safety Requirements
- e) IEC 60065, The International Electro-technical Commission (IEC).
Audio, Video and Similar Electronic Apparatus - Safety Requirements

8-2. Environment

- a) RoHS, Commission Delegated Directive (EU) 2015/863 of 31 March 2015 amending Annex II to Directive 2011/65/EU of the European Parliament and of the Council

9. Packing

9-1. Information of B/Ass'y Label

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M	N
----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------	----------

A,B,C : SIZE(INCH)

E : MONTH

G ~ N : SERIAL NO.

D : YEAR

F : DATE

notes

1. YEAR

Year	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Mark	L	M	M	P	Q	R	S	T	U	V

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

3. DATE

DATE	1 ~ 9	10 ~ 31
MARK	1 ~ 9	A ~ X

* Except "I" and "O"

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the front side of the Left Source PCB.
This is subject to change without prior notice.

9-2. Packing Form

a) Package quantity in one Pallet :272pcs

b) Pallet Size :1140 mm(L) X 910 mm(W) X 1080mm(H)

10. Precautions

Please pay attention to the followings when you use this TFT LCD module.

10-1. Handling Precautions

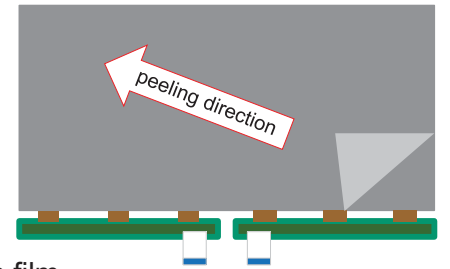
- (1) Please attach the surface transparent protective film to the surface in order to protect the polarizer.
Transparent protective film should have sufficient strength in order to resist external force.
- (2) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (3) In order to prevent the scratch, do not contact the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.
(Some cosmetics are detrimental to the polarizer.)
- (4) After removing the protective film, when the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petroleum benzine.
Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (5) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (6) Since a module is composed of electronic circuits, it is not strong to electrostatic discharge.
Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly. Panel ground path should be connected to metal ground.
- (7) Please make sure to avoid external forces applied to the Source PCB and D-IC during the process of handling or assembling the TV set. If not, it causes panel damage or malfunction.
- (8) Panel and BLU should be protected from the static electricity. If not, it causes IC damage.
- (9) Do not pull or fold the source D-IC which connects the source PCB and the panel.
- (10) Panel(board ass'y) should be put on the BLU structure precisely to avoid mechanical impact.
- (11) FFC Cable should be connected between System board and Source PCB correctly.
- (12) Mechanical structure for backlight system should be designed for sustaining board ass'y safely.
- (13) Surface temperature of the Component on PCB should be controlled under 110℃ (D-IC : 110℃) with TV Set status.
If not, problems such as IC damage or decrease of lifetime could occur.

10-2. Operating Precautions

- (1) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (2) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, Stable time (required time that brightness is stable after turned on) becomes longer
- (3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.

10-3. Protection Film

- (1) Please don't remove the protection film before assembly.
- (2) Please peel off the protection film slowly.
- (3) Please peel off the protection film just like shown in the Fig.1
- (4) Ionized air should be blown over during the peeling.
- (5) Source PCB should be connected to the ground when peel off the protection film.
- (6) The protection film should not be contacted to the source D-IC during peeling it off.

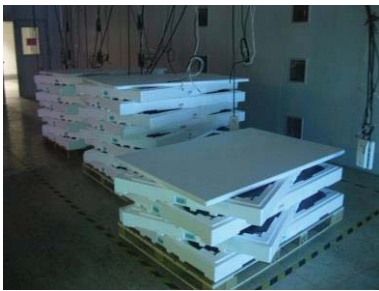


< Fig. 1 >

10-4. B/A Box Pretreatment Precautions

In winter season , in particular, please be aware of the following precautions.

- (1) Before putting B/A boxes on the line, aging process is required to make the temperature of products similar to the temperature of workplace.
- (2) Place the lid open on the B/A box and allowed to stand for 24 hours in the similar environment of work place. It was shown in Fig.1

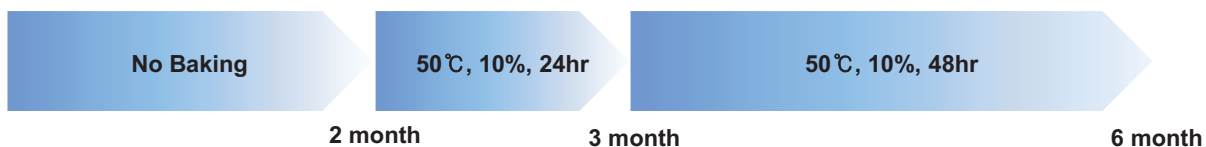


< Fig. 1 >

10-5. Storage Precautions

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Temperature : 5 ~ 40 °C
- (2) Humidity : 35 ~ 75 %RH
- (3) Period : 6 months
- (4) Control of ventilation and temperature is necessary.
- (5) Please make sure to protect the product from strong light exposure, water or moisture. Be careful for condensation.
- (6) Please keep the modules at a circumstance shown below Fig. 2



< Fig. 2 >

10-6. Packing Precautions

Product assembled into module should be stored in the Al-bag(cover case).

10-7. Appropriate Condition for Commercial Display

- Generally large-sized LCD modules are designed for consumer applications (TV).
Accordingly, a long-term display like in Commercial Display application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.

1. Normal operating condition

- Temperature: 0 ~ 40℃
 - Operating Ambient Humidity : 10 ~ 90 %
 - Display pattern: dynamic pattern (Real display)
- Note) Long-term static display can cause image sticking.

2. Operating usages under abnormal condition

a. Ambient condition

- Well-ventilated place is recommended to set up Commercial Display system.

b. Power and screen save

- Periodical power-off or screen save is needed after long-term display.

3. Operating usages to protect against image sticking due to long-term static display

- a. Suitable operating time on static image : Under **18(or 24)** hours a day under the condition of 350nit B/L.
(The moving picture can be allowed for **24 hours a day**)

APPENDIX-I-1

■ Pallet Ass'y

TBD

APPENDIX-I-2

■ Control PCB Packing Ass'y

TBD

APPENDIX- II-1

■ Serial Label

TBD

■ Control PCB Label

TBD

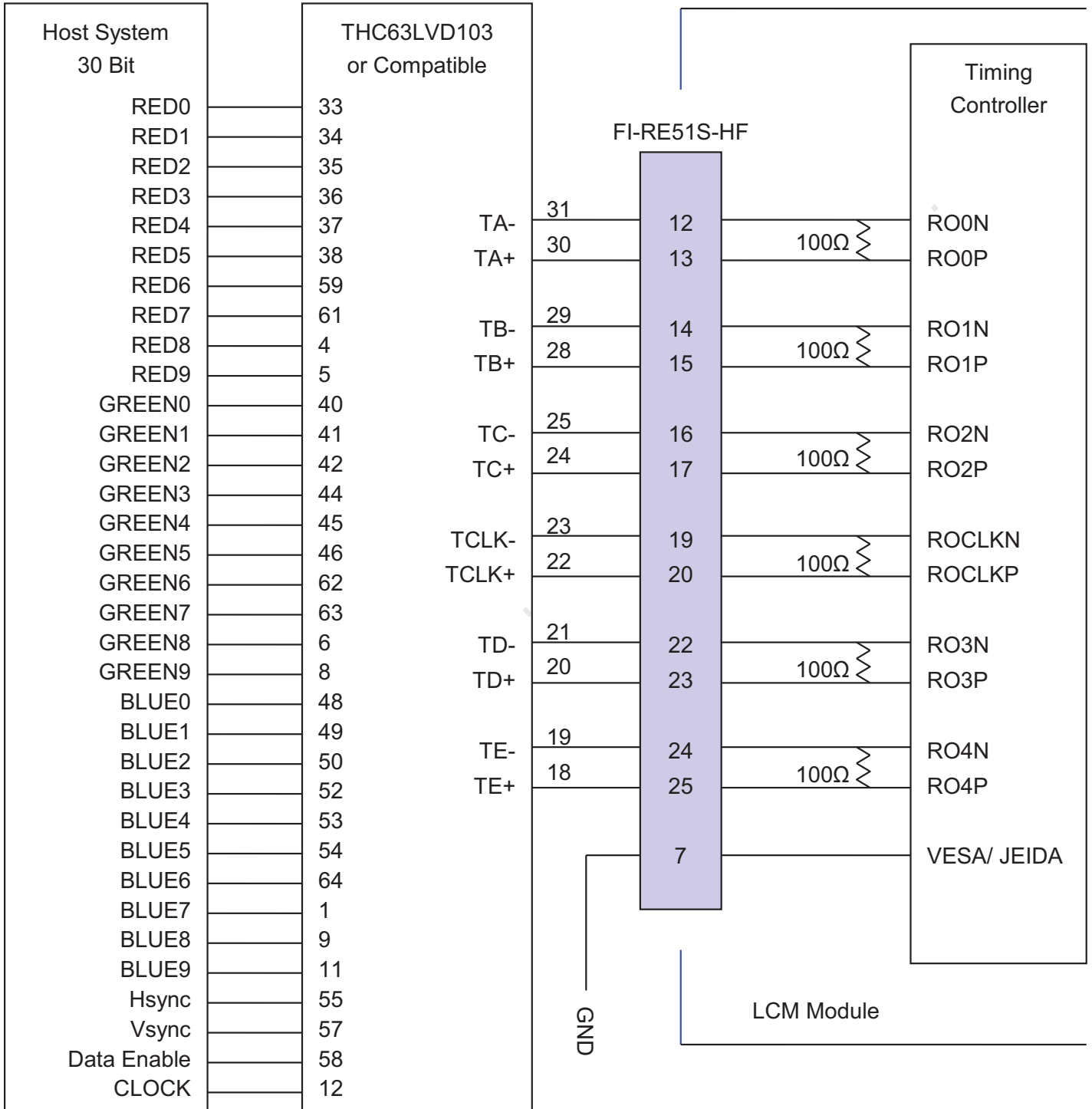
APPENDIX- II-2

■ BOX Label

TBD

APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "L" or "NC")



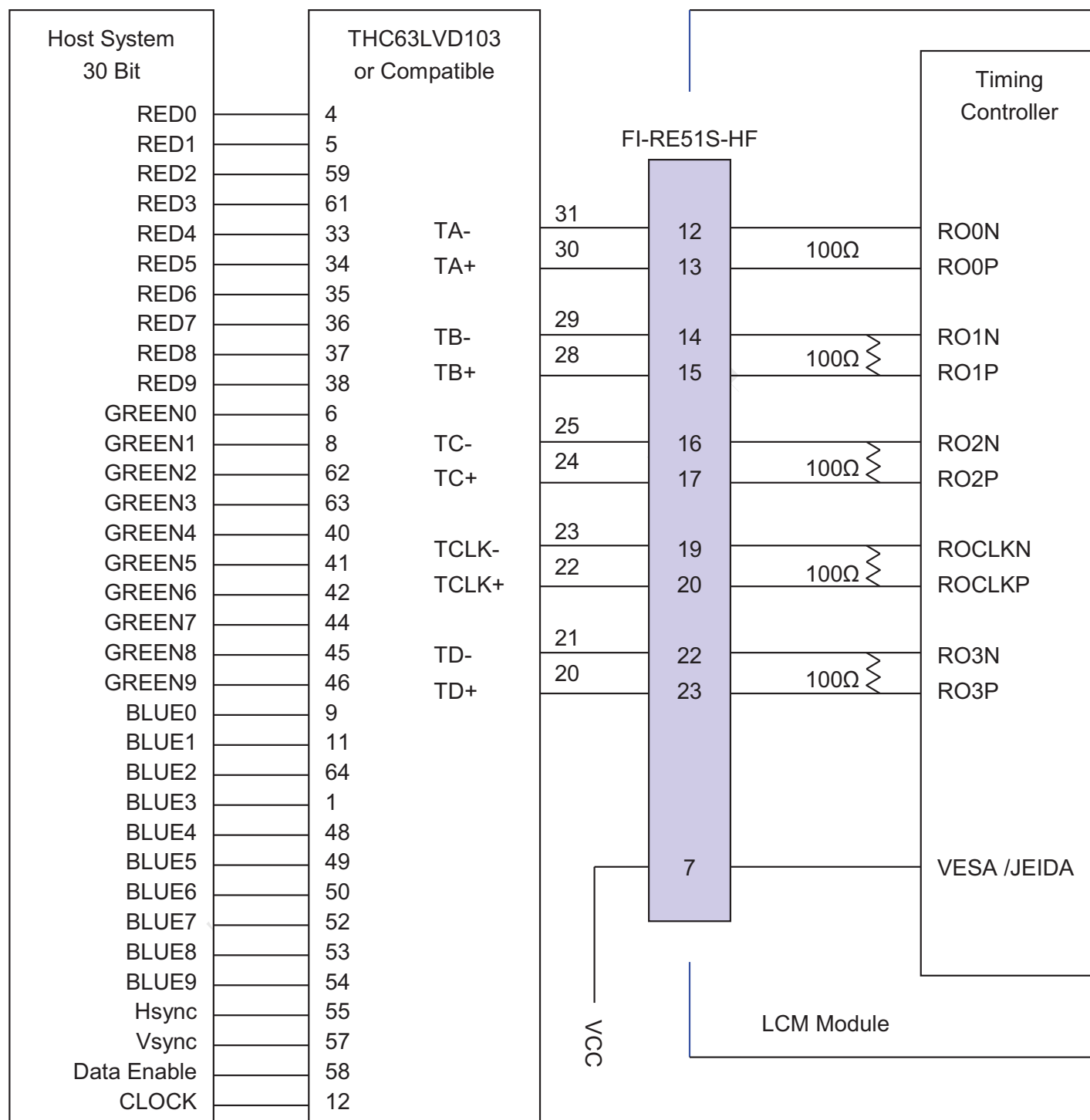
Note: 1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '9' means MSB and '0' means LSB at R,G,B pixel data.

APPENDIX- III-2

■ Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7= "H")



Note :1. The LCD module uses a 100 Ohm[Ω] resistor between positive and negative lines of each receiver input.

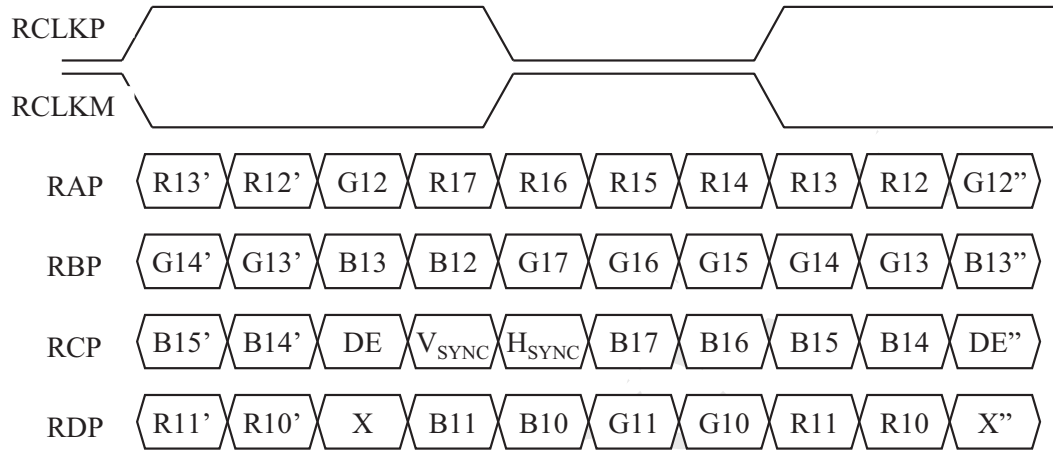
2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)

3. '7' means MSB and '0' means LSB at R,G,B pixel data.

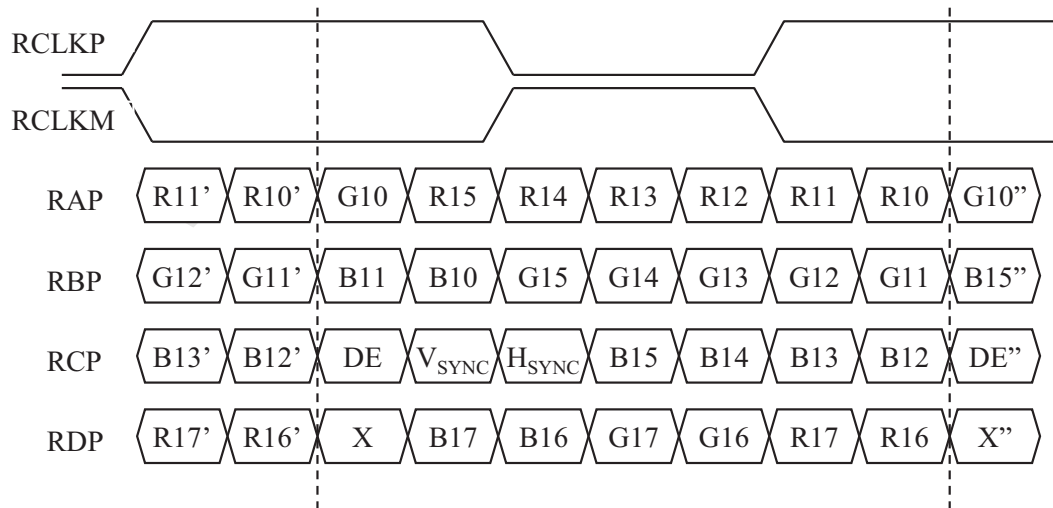
APPENDIX- IV

■ LVDS Data-Mapping Information (8 Bit)

1) LVDS Select : “H” Data-Mapping (JEIDA format)



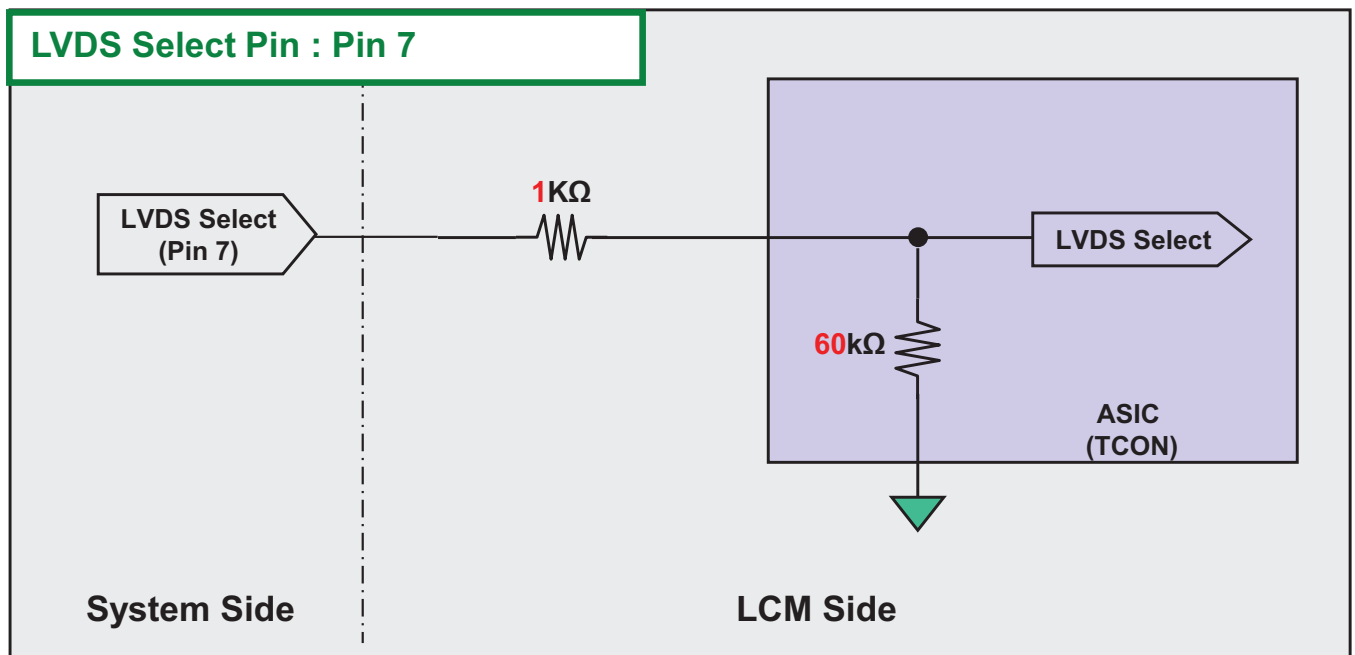
2) LVDS Select : “L” Data-Mapping (VESA format)



APPENDIX- V

■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



APPENDIX- VI

■ Flicker Adjustment

Parameter	Unit	Min	Typ	Max	Note
Inversion Method	-	V2-Dot Inversion			
Adjust Pattern / Gray Level	-	V2Dot Green Flicker / 223Gray			60Hz
Position	-	Center			
Voltage range	V	5.76(TBD)	6.26 (TBD)	6.76(TBD)	Writing Setting Value

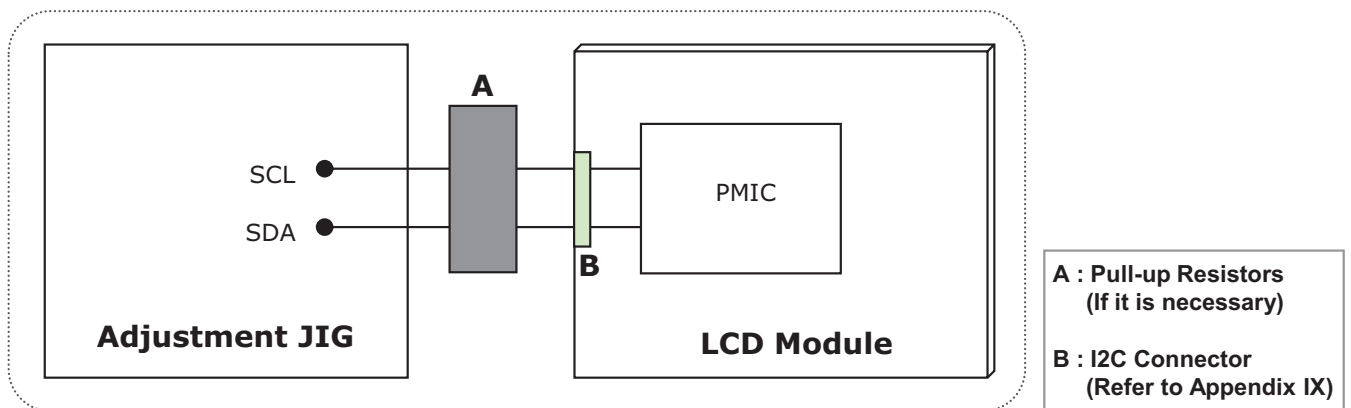
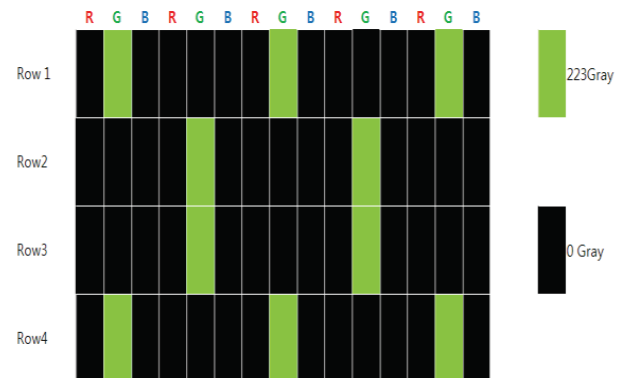
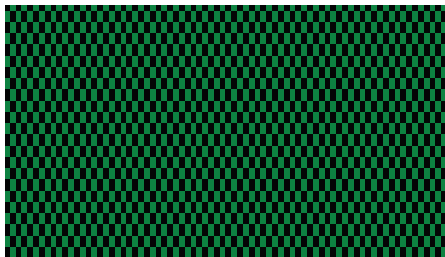


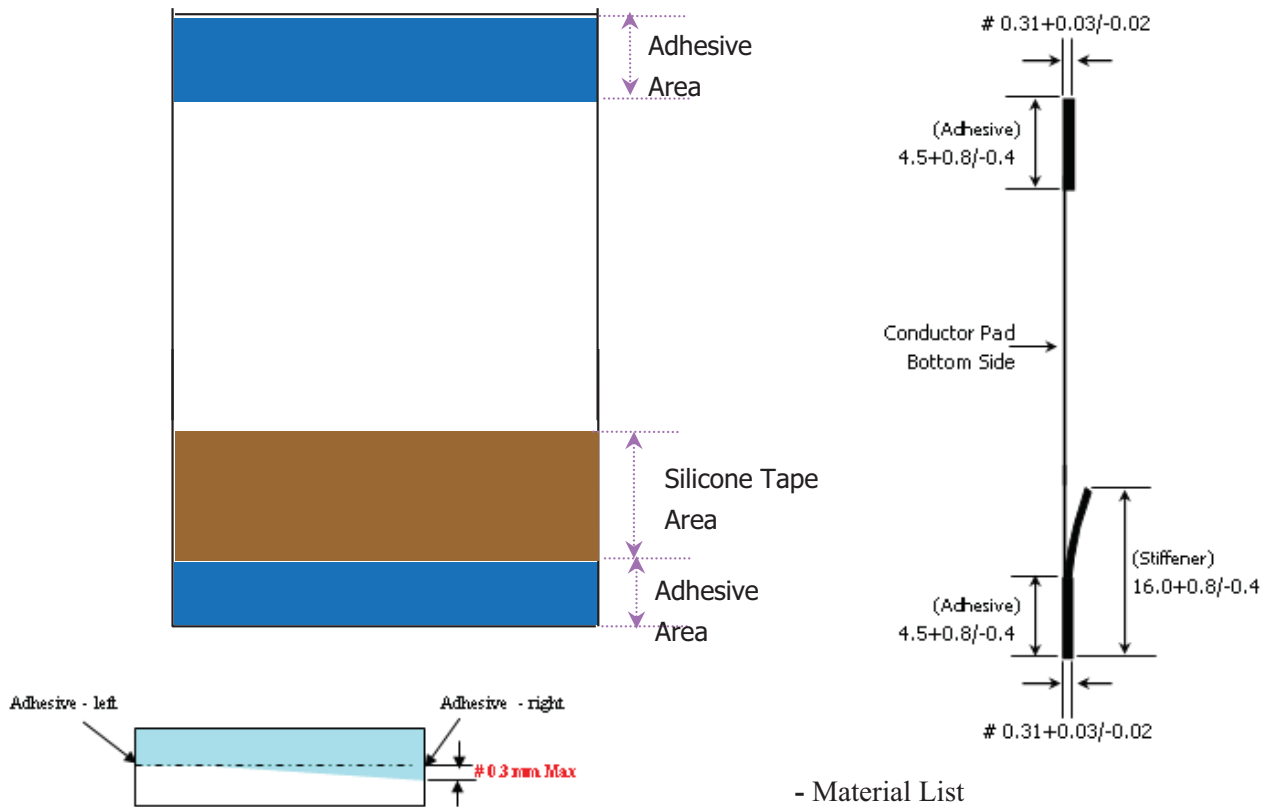
FIG.7 VCOM Adjustment Pattern & Block Diagram

Product Specification

APPENDIX-VII-1

It is recommended to use between Control board and Source PCB.

- LCD Connector: TF06L-50S-0.5SH (Manufactured by Hirose) or PF050-L50B-C21 (Manufactured by UJU) or GF05G-50S-E2000 (Manufactured by LS Cable)



- Material List

APPLICATION	STANDARD	MATERIAL	REFERENCE
1. CONDUCTOR	GOLD PLATED COPPER WIRE	1. 99.99% COPPER 2. GOLD PLATED	1. Tolerance - THICKNESS: ± 0.01 - WIDTH: $+0.04 -0.02$ 2. ELONGATION(%): 10 MORE THAN 3. TENSION: (KGF) 0.2 MORE THAN 4. GOLD THICKNESS : 0.05 μ m MIN *MAKER : DOSOL Processing : EunSung
2. INSULATION	POLYESTER FILM (PET/PET)	1. POLYESTER BASE FILM : 0.025 mm 2. POLYESTER HOTMELT ADHESIVE : 0.035 mm TOTAL THICKNESS : 0.060 mm	UL VW-1 FLAME Width : 120mm Length : 500M UNIT : ROLL *MAKER : COSMOAMT/ SHINCHANG HOTMELT
3. SUPPORTING TAPE	POLYESTER FILM	1. POLYESTER BASE FILM : 0.188 mm 2. POLYESTER HOTMELT ADHESIVE : 0.027 mm TOTAL THICKNESS : 0.215 mm	Width : 20.5mm Length : 250M Unit : ROLL *MAKER : SUNGSHIN Trading/ COSMOAMT
4. SILICON TAPE (BROWN)		TOTAL THICKNESS : 0.065 mm	*MAKER : DAEHYUN ST

◆ Note

- Layer : Single Side
- Pad : GOLD Plating
- # e dimension : Cpk 1.0 more
- ## dimension : Cpk 1.33 more
- Stiffener Color : Sky Blue
- H-F
- Dimension Unit : mm

APPENDIX-VII-2

■ FFC CONNECTOR INFORMATION (Source PCB)

- LCD Connector : TF06L-50S-0.5SH (Manufactured by HRS) or Compatible

■ Drawing (TF06L-50S-0.5SH_Manufactured by HRS)

